

FIG 1

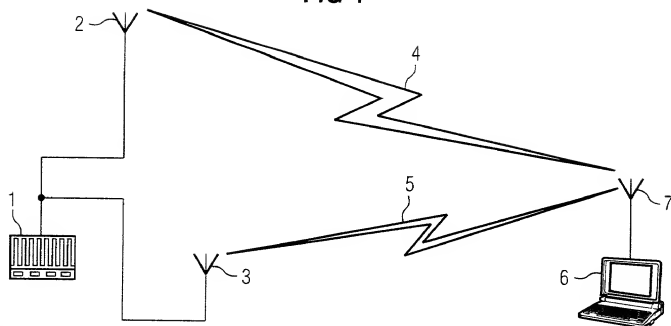


FIG 2

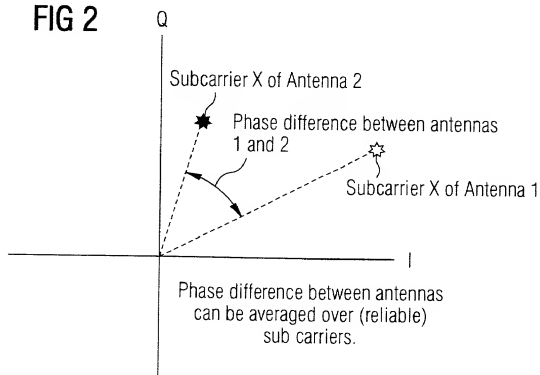


FIG 3

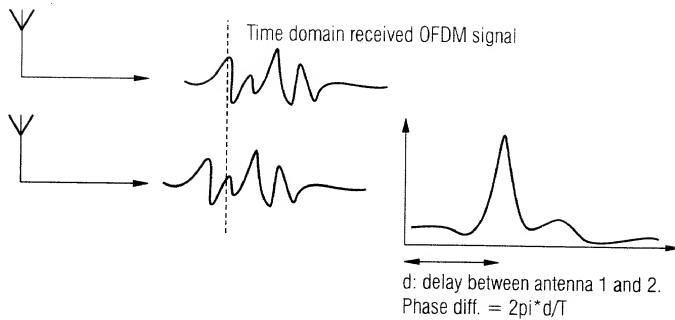


FIG 4

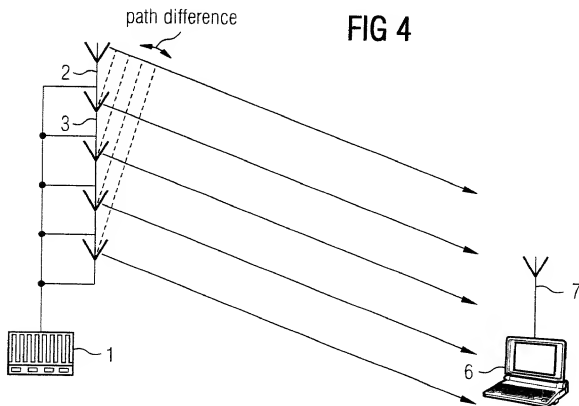


FIG 5

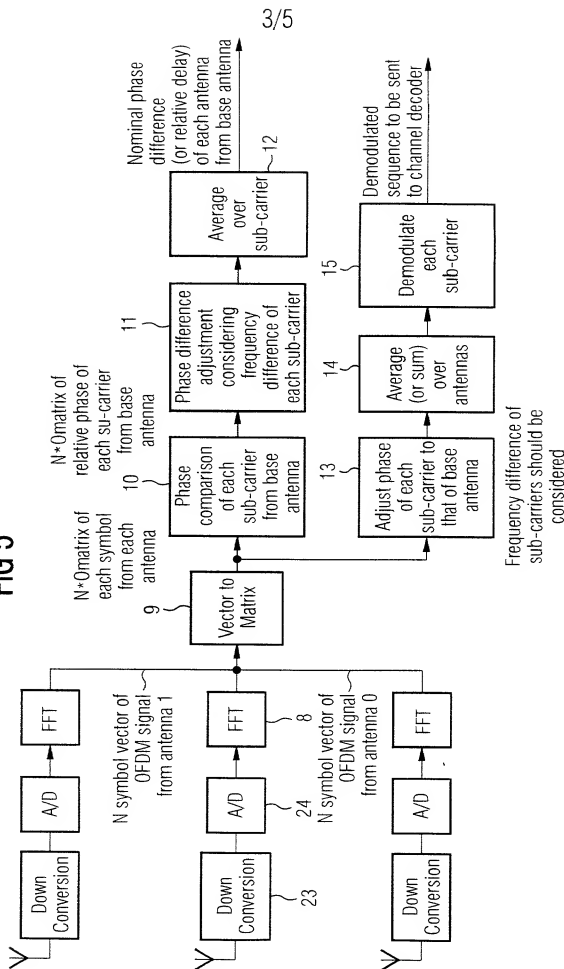


Figure 1 is a block diagram of a multi-antenna system. A digital baseband signal after IFFT enters a D/A converter (16), then an Up Conversion block (17). The output splits into multiple paths, each passing through a Time Delay block (18) before being transmitted by an antenna (Antenna 1, Antenna 2, ..., Antenna Q).

FIG 7

Symbol vector of each sub-carrier symbol

Phase Adjustment 1

IFFT

D/A

Up Conversion

Antenna 1

Phase Adjustment 2

IFFT

D/A

Up Conversion

Antenna 2

Phase Adjustment Q

IFFT

D/A

Up Conversion

Antenna Q

FIG 8

State of the Art

